

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Hideto HIDAKA	:	Confirmation Number: Not yet assigned
	:	
Serial No.: Divisional of Appln.	:	
Serial No. 09/776,681	:	Group Art Unit: Not yet assigned
	:	
Filed: October 08, 2003	:	Examiner: Not yet assigned
	:	
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE OPERATING WITH LOW POWER CONSUMPTION	:	

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

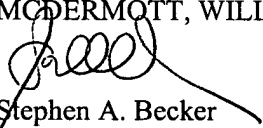
Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The references were cited by or submitted to the U.S. Patent and Trademark Office in parent application Serial No. 09/776,681, filed February 6, 2001, which is relied upon for an earlier filing date under 35 USC 120. Thus, copies of these references are not attached. 37 CFR 1.98(d).

Respectfully submitted,  
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<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  (PTO-1449)				ATTY. DOCKET NO. <b>57454-973</b>		SERIAL NO. <b>Divisional of Appln. Serial No. 09/776,681</b>			
				APPLICANT <b>Hideto HIDAKA</b>					
				FILING DATE <b>October 08, 2003</b>		GROUP <b>Not yet assigned</b>			
<b>U.S. PATENT DOCUMENTS</b>									
EXAMINER'S INITIALS	CITE NO.	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
		Number-Kind Code <sup>2</sup> (if known)							
		US	5,610,533	3/11/97	Arimoto et al.				
		US	5,880,604	3/9/99	Kawahara et al.				
		US	5,942,784	8/24/99	Harima et al.				
		US	5,909,140	6/1/99	Choi				
		US	3,806,741	4/74	Smith				
		US	4,672,243	6/87	Kirsch				
		US	5,512,771	4/96	Hiroki et al.				
		US	5,985,706	11/99	Gilmer et al.				
		US	6,133,762	10/00	Hill et al.				
		US	6,255,698	7/01	Gardner et al.				
		US	6,307,234	10/01	Ito et al.				
		US	2002/0000872 A1	1/02	Ye et al.				
		US	2002/0079542 A1	6/02	Trivedi et al.				
		US							
<b>FOREIGN PATENT DOCUMENTS</b>									
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear		Translation	
		Country Codes - Number 4 - Kind Codes (if known)						Yes	No
		JP	6-237164	8/23/94					
		JP	11-150193	6/2/99					
		EP	0986177	3/15/00					
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>									
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.							
		"Ultra LSI Memory," by K. Ito, Baifukan, 11/5/94 (First Edition), pp. 351-371.							
EXAMINER					DATE CONSIDERED				

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.  
 1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.